

SFP-BLxxT5-80DC

25Gb/s SFP28 ZR BIDI 80km DDM Transceiver

PRODUCT FEATURES

- **Support data rate up to 25.78125Gb/s**
- **Hot-Pluggable SFP Footprint and Single LC Connector**
- **Up to 80km reach for G.652 SMF**
- **EML laser and Integrated SOA & PIN TIA ROSA**
- **RoHS 6 compliance**
- **Compliant to IEEE 802.3cc, SFF-8472 and SFF-8419**
- **Complies with EU Directive 2015/863/EU**
- **Compliant with SFP+ MSA**
- **Power consumption:2.5W**
- **Temperature Range:0°C ~70°C**

APPLICATIONS

- **25G Ethernet**
- **CPRI option 10**

DESCRIPTIONS

The SFP-BLxxT5-80DC is a single-channel, Pluggable, Fiber-Optic SFP28 for 25 Gigabit Ethernet and Infiniband EDR Applications. It is a high performance module for short-range data communication and interconnect applications which operate at 25.78125 Gbps up to 80km. They are compliant with SFF-8431, SFF-8432. The transmitter converts serial CML electrical data into serial optical data. The receiver converts serial optical data into serial CML electrical data. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Storage Temperature	T _{stg}	-40		+85	°C	
Case Operating Temperature(Commercial)	T _O	0		70	°C	
Relative Humidity - Storage	R _{HS}	5		95	%	
Relative Humidity - Operating	R _{HO}	5		85	%	
DC Supply Voltage	V _{CC}	0		3.6	V	

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Case Operating Temperature	Top	0		70	°C	
Power Supply Voltage	VCC	3.13	3.3	3.47	V	
Transmission Distance	TD			80	km	Over SMF

Electrical Characteristics

High-Speed Signal: Compliant to CEI-25G-VSR

Low-Speed Signal: Compliant to SFF-8419

Parameter		Symbol	Min.	Typical	Max.	Unit	Notes
Transmitter (Module Input)							
Differential Input Resistance		R _{Rdin}	90	100	110		
Input Differential Voltage		R _{Vdiff}			900	mVpp	
Tx_Disable	Normal Operation	V _{IL}	-0.3		0.8	V	
	Laser Disable	V _{IH}	2.0		VCC+0.3	V	
Receiver (Module Output)							
Differential Resistance		T _{Rd}	90	100	110	Ohm	
Output Differential Voltage		T _{Vdiff}			900	mVpp	
Differential Termination Resistance Mismatch		T _{Rdm}			10	%	
Rx los	Normal Operation	V _{OL}	-0.3		0.4	V	
	Loss Signal	V _{OH}	2		VCCHOST	V	

Optical and Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Transmitter						
Optical Modulation Amplitude(OMA)	POMA	2		8	dBm	
Average Output Power	POUT	2		7	dBm	
Average Output Power(Laser Off)	POFF			-30	dBm	
Wavelength	λ	1281	1295	1297	nm	
		1306	1309	1322		
Spectrum Bandwidth @ -20dB	$\Delta\lambda$			1	nm	
Side mode suppression ratio(SMSR)	SMSR	30			dB	
Extinction ratio	ER	8			dB	
Transmitter and dispersion penalty(TDP)				2.7	dB	
RIN _{20OMA}	RIN			-130	dB/Hz	
Receiver						
Wavelength	λ	1306	1309	1322	nm	
		1281	1295	1297		
Received Sensitivity	P _{SEN}			-24	dBm	1
Optical Power Overload	P _{IN(SAT)}	-4			dBm	
Damage threshold		3			dBm	2
Rx_LOS of Signal Assert	P _A	-40			dBm	
Rx_LOS of Signal De-assert	P _D			-28	dBm	
Rx_LOS of Signal Hysteresis	P _{Hy}	0.5		5	dB	
Optical Return Loss Tolerance	ORLT	20			dB	

Notes:

1. Test pattern: PRBS31. BER<5x10⁻⁵;
2. The receiver shall be able to tolerate, without damage, continuous exposure to an optical input signal having this average power level. The receiver does not have to operate correctly at this input power.

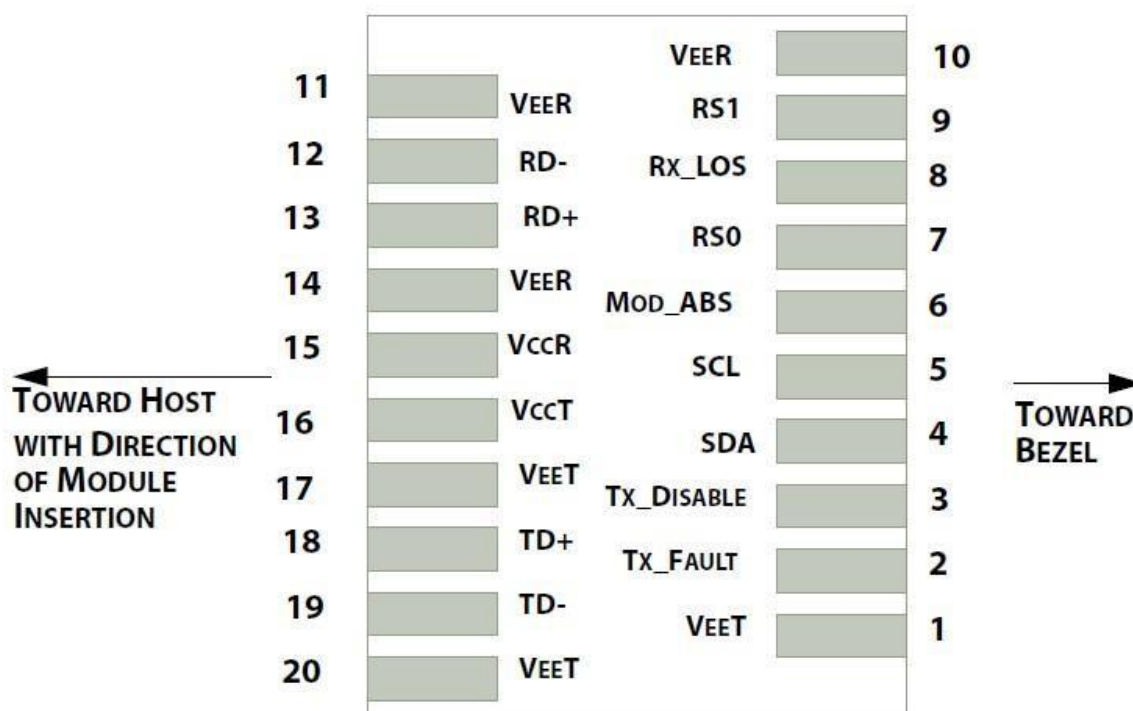
Digital Diagnostics

Parameter	Range	Accuracy	Unit	Calibration
Temperature	0 to 70	±3	°C	Internal
Voltage	3.13 to 3.47	±3%	V	Internal
Tx Bias Current	0 to 100	±10%	mA	Internal
Tx Output Power	2 to 7	±3	dB	Internal
Rx Input Power	-24 to -4	±3	dB	Internal

Communication Interface Timing Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
TX_Disable Assert Time	t_off			100	us	
TX_Disable Negate Time	t_on			2	ms	
Time to Initialize Include Reset of TX_FAULT	t_int			300	ms	
TX_FAULT from Fault to Assertion	t_fault			100	us	
TX_Disable Time to Start Reset	t_reset	10			us	
Receiver Loss of Signal Assert Time	T _A ,RX_LOS			100	us	
Receiver Loss of Signal Deassert Time	T _d ,RX_LOS			100	us	
Rate-Select Chage Time	t_ratesel			10	us	

Pin Diagram



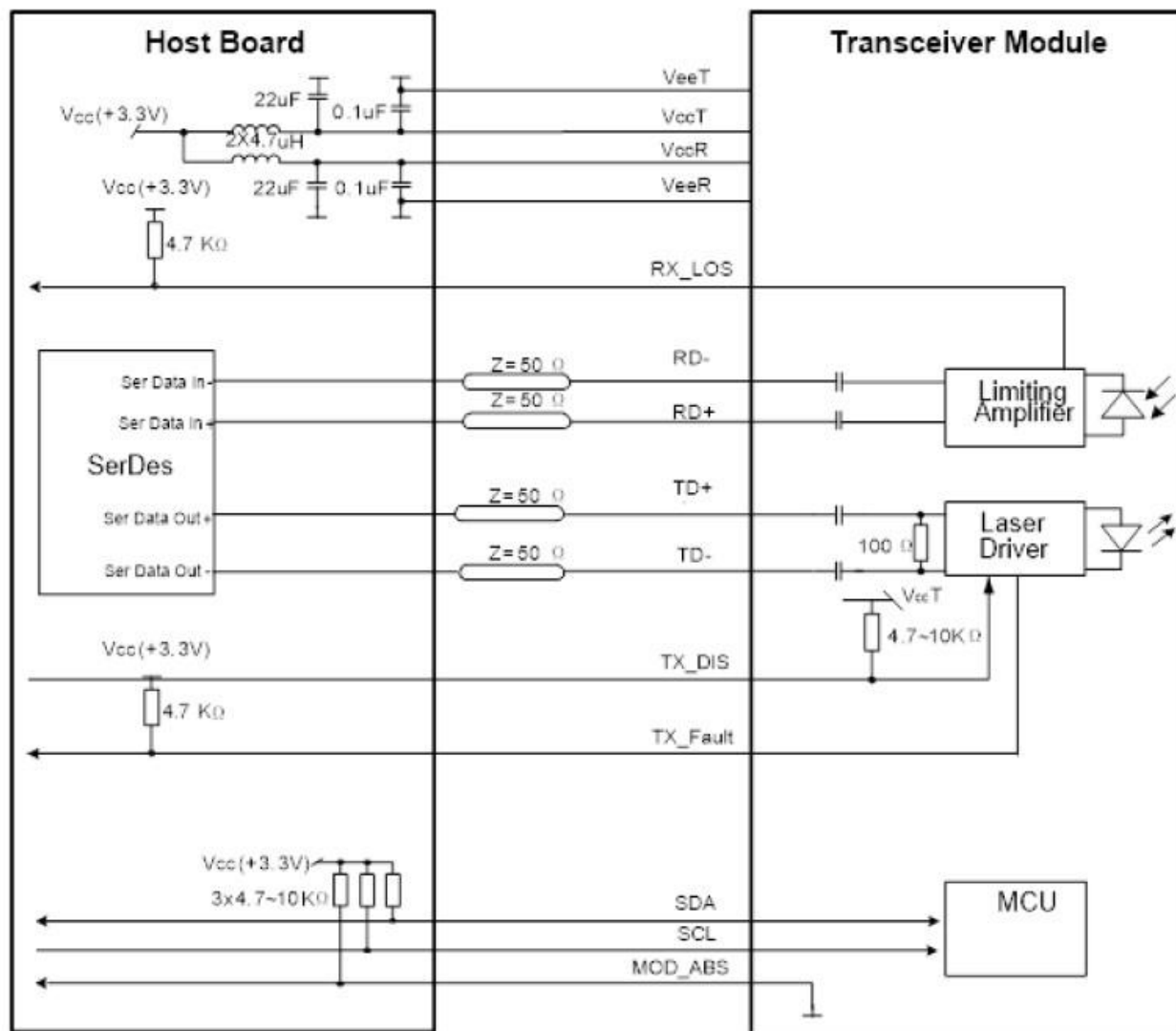
Pin Definitions

PIN #	Name	Function	Notes
1	VeeT	Transmitter Ground	1
2	Tx Fault	Transmitter Fault - High indicates a fault condition	2
3	Tx Disable	Transmitter Disable – High or open disables the transmitter	
4	SDL	2-wire Serial Interface Data Line (MOD-DEF2)	3
5	SCL	2-wire Serial Interface Clock (MOD-DEF1)	3
6	MOD-ABS	Module Absent, connected to VeeT or VeeR in the module	
7	RS0	Rate Select 0	5
8	RX_LOS	Receiver Loss of Signal(LVTTL-O). Logic 0 indicates normal operation	4
9	RS1	Rate Select 1	1
10	VeeR	Receiver Ground	1
11	VeeR	Receiver Ground	1
12	RD-	Inverse Received Data out (CML-O), AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	VeeR	Receiver Ground	1
15	VccR	Receiver Power Supply	
16	VccT	Transmitter Power Supply	
17	VeeT	Transmitter Ground	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	VeeT	Transmitter Ground	1

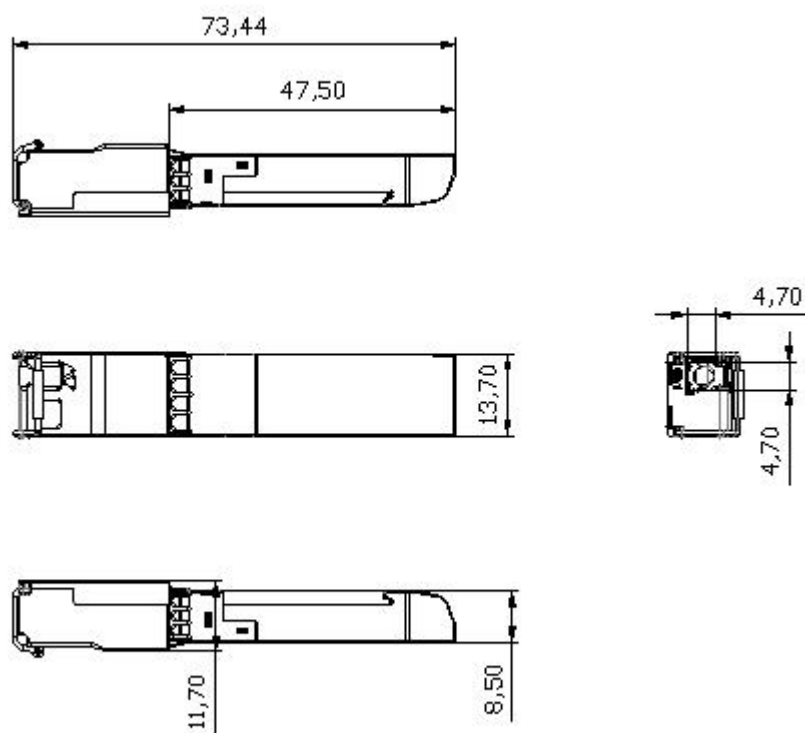
Notes:

- Module ground pins GND are isolated from the module case.
- Tx_Fault is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on Host board.
- Should be pulled up with 4.7k–10kohms on host board to a voltage between 2.0V and 3.6V.
- LOS is open collector output. Should be pulled up with 4.7k–10kohms on host board to a voltage between 2.0V and 3.6V.
- RS0 and RS1 pins are pulled low to GND with a resistor > 30KΩ in module.

Recommended Interface Circuit



Mechanical Diagram



Ordering information

Part Number	Product Description
SFP-BL23T5-80DC	Tx1295/Rx1309nm, 25.78Gbps BIDI, LC, 80km, 0°C~+70°C, with DDM
SFP-BL32T5-80DC	Tx1309/Rx1295nm, 25.78Gbps BIDI, LC, 80km, 0°C~+70°C, with DDM

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